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CONFIRMATION NO. 4868

SERIAL NUMBER 10/531,287	FILING OR 371(c) DATE 04/14/2005 RULE	CLASS 716	GROUP ART UNIT 2825	ATTORNEY DOCKET NO. TAM-103
APPLICANTS Tadaaki Tanimoto, Foothill RANCH, CA; Masurao Kamada, Higashimurayama, JAPAN;				
** CONTINUING DATA ***** This application is a 371 of PCT/JP03/12839 10/07/2003				
** FOREIGN APPLICATIONS ***** JAPAN 2002-300073 10/15/2002				
Foreign Priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 (a-d) conditions <input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance Verified and Acknowledged _____ Examiner's Signature _____ Initials _____		STATE OR COUNTRY CA	SHEETS DRAWING 62	TOTAL CLAIMS 20
				INDEPENDENT CLAIMS 5
ADDRESS 24956				
TITLE Compiler and logic circuit design method				
FILING FEE RECEIVED 1550	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:		<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit	